



UTTARAKHAND OPEN UNIVERSITY, HALDWANI (NAINITAL)  
उत्तराखण्ड मुक्त विश्वविद्यालय, हल्द्वानी (नैनीताल)

BCA-16/BCA-17 2<sup>nd</sup> YEAR 4<sup>th</sup> SEMESTER ASSIGNMENT

Session - 2017-18, Summer Last Date of Submission: 31st October 2017

Session - 2017-18, Winter Last Date of Submission: 30th April 2018

Course Title: Computer Organization

Course Code: BCA-11

Maximum Marks: 30/20

Section 'A' contains 08 short answer type questions of 4/2.5 marks each. Learners are required to answers 4 questions only. Answers of short answer-type questions must be restricted to 250 words approximately.

1. Distinguish between combinational circuit and sequential circuit.
2. What do you mean by multiplexer? With diagram explain the working of a 8-to-1 multiplexer.
3. What are the differences between asynchronous and synchronous counter?
4. What is the task of the DMA Controller?
5. What do you mean by addressing mode? Discuss the different addressing mode of 8085 with example.
6. What do you mean by fetch cycle and execute cycle?
7. Differentiate between Static RAM and Dynamic RAM.
8. What is the function of cache memory?

Section 'B' contains 04 long answer-type questions of 7/5 marks each. Learners are required to answers 02 questions only.

1. Briefly describe Main Memory.
2. Explain ROM and its different types.
3. What is pipelining? Explain the types of pipelining?
4. What are the main components of CPU? Briefly describe each of these components.