



UTTARAKHAND OPEN UNIVERSITY, HALDWANI (NAINITAL)  
उत्तराखण्ड मुक्त विश्वविद्यालय, हल्द्वानी (नैनीताल)

PGDCA 1<sup>ST</sup> YEAR 2<sup>ND</sup> SEMESTER ASSIGNMENT

*Last Date of Submission: 15 July 2015*

**Course Title:** Computer Organization and Architecture

**Course Code:** PGDCA-05

**Year:** 2014-15

**Maximum Marks:** 40 Marks

Section 'A' contains 08 short answer type questions of 5 marks each. Learners are required to answer 4 questions only. Answers of short answer-type questions must be restricted to 250 words approximately.

1. Define the term Computer Architecture.
2. What is the function of cache memory?
3. What is Bus? Draw the single bus structure.
4. What is Bus? Draw the single bus structure.
5. What is register?
6. Write down the operation of control unit?
7. What is SRAM and DRAM?
8. What is the role of MAR and MDR?

Section 'B' contains 04 long answer-type questions of 10 marks each. Learners are required to answer 02 questions only.

1. Explain with the block diagram the DMA transfer in a computer system.
2. What is RISC? Explain with proper example.
3. Describe the functions of the I/O Interface units in a computer.
4. What do you mean by Addressing Mode? Discuss different types of addressing modes including the advantages and disadvantages of each mode.