



UTTARAKHAND OPEN UNIVERSITY, HALDWANI (NAINITAL)
PGDCA 2nd SEMESTER ASSIGNMENT

Last Date of Submission: 15 May,2014

Course Title: Computer Organization and Architecture

Course Code: PGDCA-05

Year: 2013-14

Maximum Marks: 40 Marks

Section 'A' contains 08 short answer type questions of 5 marks each. Learners are required to answers 4 questions only. Answers of short answer-type questions must be restricted to 250 words approximately.

1. What is cache memory?
2. What is the difference between an assembler and compiler?
3. What is the difference RISC and CISC Processors?
4. What is the difference between Primary memory and Secondary Memory?
5. What do you understand by the terms BIOS? Explain one simple function of BIOS.
6. Difference between Synchronous and Asynchronous data transfer.
7. What is bootstrap loader?
8. What is the difference between parallel processing and pipeline processing?

Section 'B' contains 04 long answer-type questions of 10 marks each. Learners are required to answers 02 questions only.

1. Explain the concept of Virtual Memory.
2. Explain DMA with an example.
3. What are addressing modes? Explain the various addressing modes with examples.
4. Discuss the basic concepts of pipelining.